

FORM PTO-1449

Atty. Docket No.

Appl. No.

LIST OF DOCUMENTS CITED BY APPLICANT

XA-10097

28/501391

Applicant

Michitaro KANAMITSU et al.

Filing Date

Group

Herewith

2827

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
AT	AH	11-250681	9/17/99	Japan			No
I	AI	7-307098	11/21/95	Japan			No
MT	AJ	10-188576	7/21/98	Japan			No
	AK						
	AL						
	AM						
	AN						

OTHER (including author, title, date, pertinent pages, etc.)

MT	AO	K. Imamiya et al., "MP6.6 A 130mm ² 256 Mb NAND Flash with Shallow Trench Isolation Technology", <u>1999 IEEE International Solid-State Circuits Conference</u> .					
	AP	K. Suh et al., "TA7.5: A 3.3V 32 Mb NAND Flash Memory with Incremental Step Pulse Programming Scheme", <u>1995 IEEE International Solid-State Circuits Conference</u> , pp. 128-129.					
MT	AQ	K. Imamiya et al., "TA7.6: A 35 ns-Cycle-Time 3.3V-Only 32 Mb NAND Flash EEPROM", <u>1995 IEEE International Solid-State Circuits Conference</u> , pp. 130-131.					

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M. TRAN

Date Considered

3/19/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.